What is claimed is:

1. A liquid crystal display comprising:

a first substrate;

a thin film transistor and a storage capacitor provided on the first substrate;

a second substrate;

a color filter layer on the second substrate and including a recess at a location opposite to the storage capacitor;

a common electrode on the color filter layer; and

a liquid crystal layer including spacers between the first and second substrates.

2. The liquid crystal display as claimed in claim 1, wherein the recess has a depth corresponding to a height difference caused by the storage capacitor over the first substrate.

3. The liquid crystal display as claimed in claim 1, wherein the spacers are elastic and have approximately the same diameter in an uncompressed state for ones of the spacers overlying a first region where the storage capacitor is located and ones of the spacers positioned in a second region that does not include the storage capacitor.

4. The liquid crystal display as claimed in claim 2, wherein the height difference caused by the storage capacitor is greater than an allowable elastic range of the spacers.

5. A liquid crystal display comprising:

a first substrate;

a plurality of gate lines on the first substrate;

a plurality of data lines on the first substrate, the data lines crossing the gate lines;

a thin film transistor and a pixel electrode arranged at each intersection of the gate lines and data lines;

a storage capacitor defined by an overlap of one of the pixel electrodes and one of the plurality of gate lines;

a black matrix layer formed on a second substrate opposite the first substrate for blocking transmission of light through parts of the liquid crystal display excluding the pixel electrode;

a color filter layer formed on an substantially an entire surface of the second substrate inclusive of the black matrix layer and including a recess at a location opposite the storage capacitor;

a common electrode formed on substantially an entire surface of the color filter layer; and

a liquid crystal layer including spacers formed between the first and second substrates.

6. The liquid crystal display as claimed in claim 5, wherein the recess has a depth corresponding to a height difference caused by the storage capacitor over the surface of the first substrate.

7. The liquid crystal display as claimed in claim 5, wherein ones of the spacers in a first region including the storage capacitor and ones of the spacers in a second region that does not include the storage capacitor have substantially the same diameter.

8. The liquid crystal display as claimed in claim 6, wherein the height difference caused by the storage capacitor is greater than an allowable elastic range of the spacer.

9. A display apparatus for displaying an image, comprising:

a plurality of pixel means arranged into a array between a first substrate and a second substrate, each of the plurality of pixel means for displaying a unit portion of an image corresponding to a data signal of a plurality of data signals;

first driving means for selectively applying the plurality of data signals along respective ones of a plurality of data lines provided on a first substrate;

a plurality of switching means arranged into an array on a first substrate and addressable by control signals provided along a plurality of control lines on the first substrate;

second driving means for selectively applying the control signals to the control lines;

charge storing means associated with each of the switching means and provided on the first substrate, the charge storing means for storing charge provided by one of the data lines when the switching means associated with the charge storing means is driven in an on state, each of the charge storing means forming a step structure on the first substrate;

spacer means arranged between the first and second substrates for maintaining a gap therebetween, wherein the spacer means are positioned over the step structure and in areas other than over the step structure, and the second substrate includes compensating means for accommodating at least a portion of the spacer over the step structure in correspondence with a height of the step to thereby maintain substantially uniform cell gap.

10. The display apparatus of claim 9 further comprising liquid crystal in the gap between the first and second substrates.

11. The display apparatus of claim 9, wherein the spacer means are elastic and have substantially the same size in an uncompressed state.

12. The display apparatus of claim 9, wherein the second substrate includes a color filter layer, and the compensating means is provided in the color filter layer.

13. The display apparatus of claim 9, wherein the portion of the spacer accommodated by the compensating means corresponds to at least a portion of the height of the step structure that exceeds an allowable compressive range of the spacer means.